

**FIG. 1**

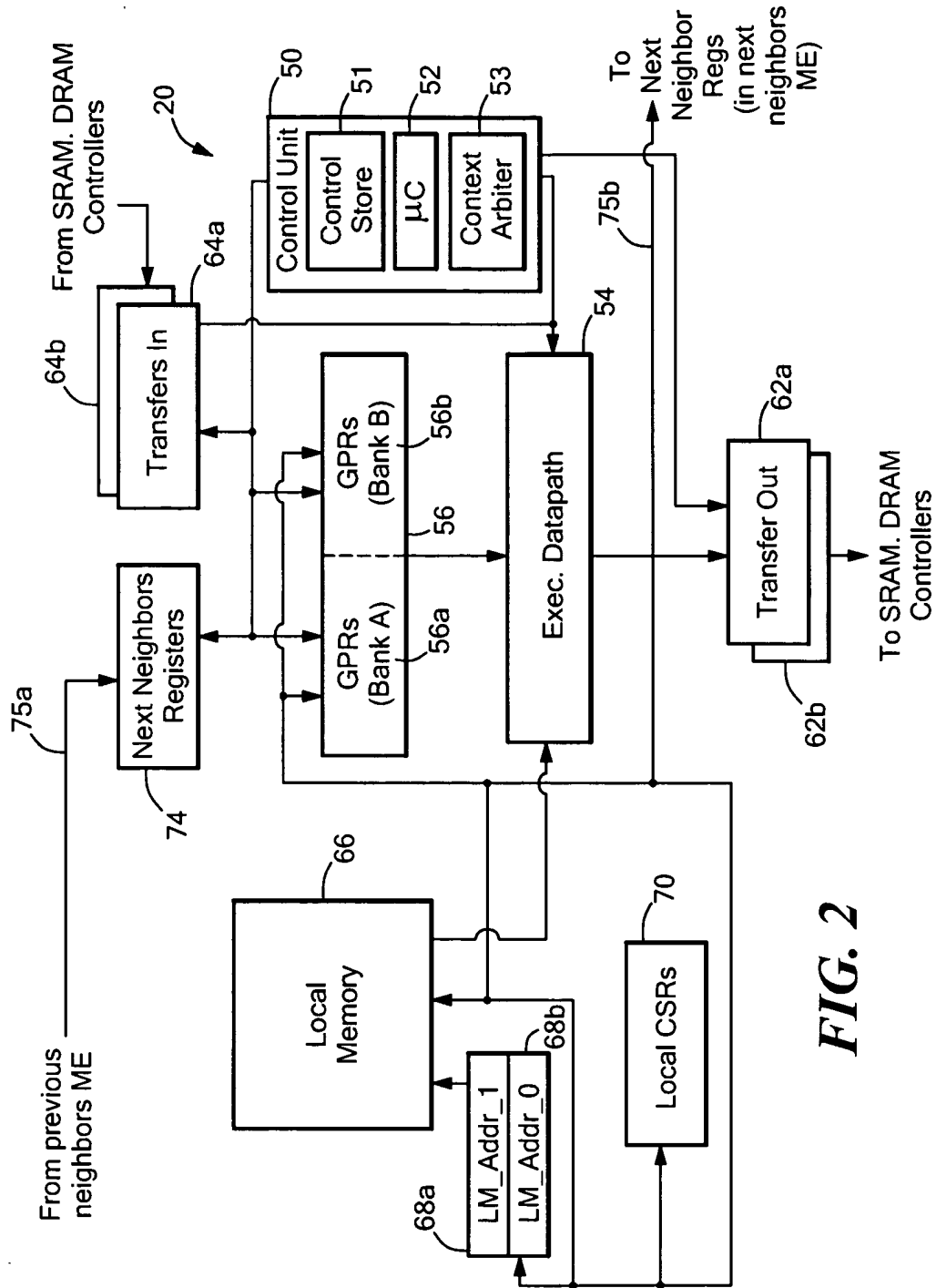
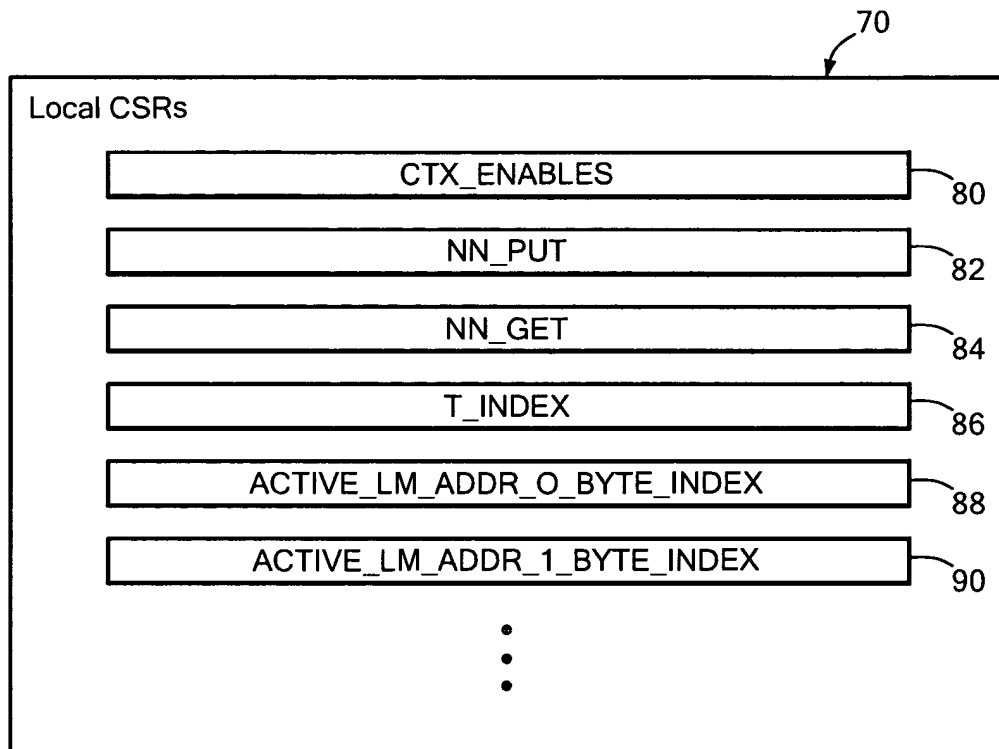
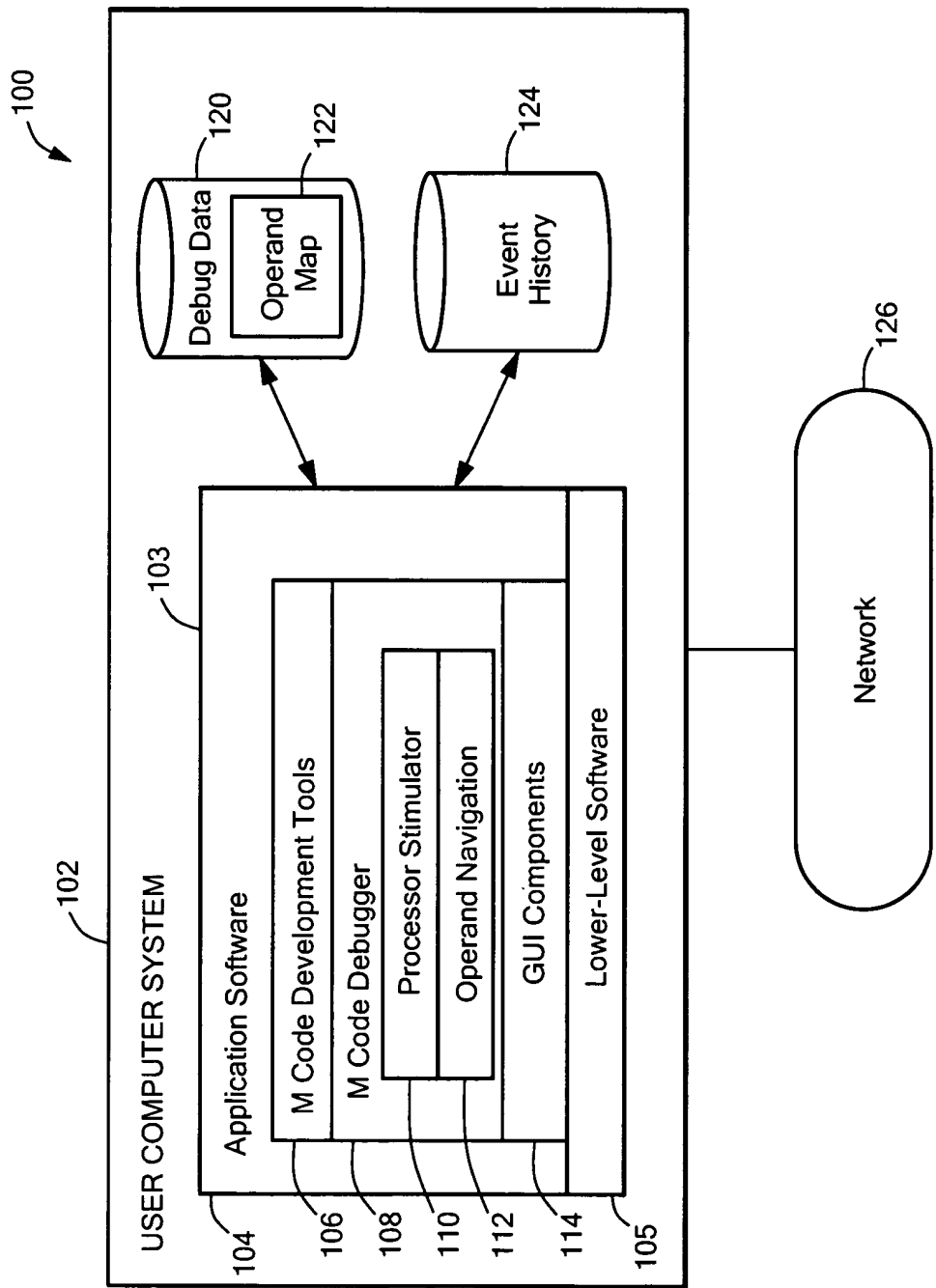


FIG. 2

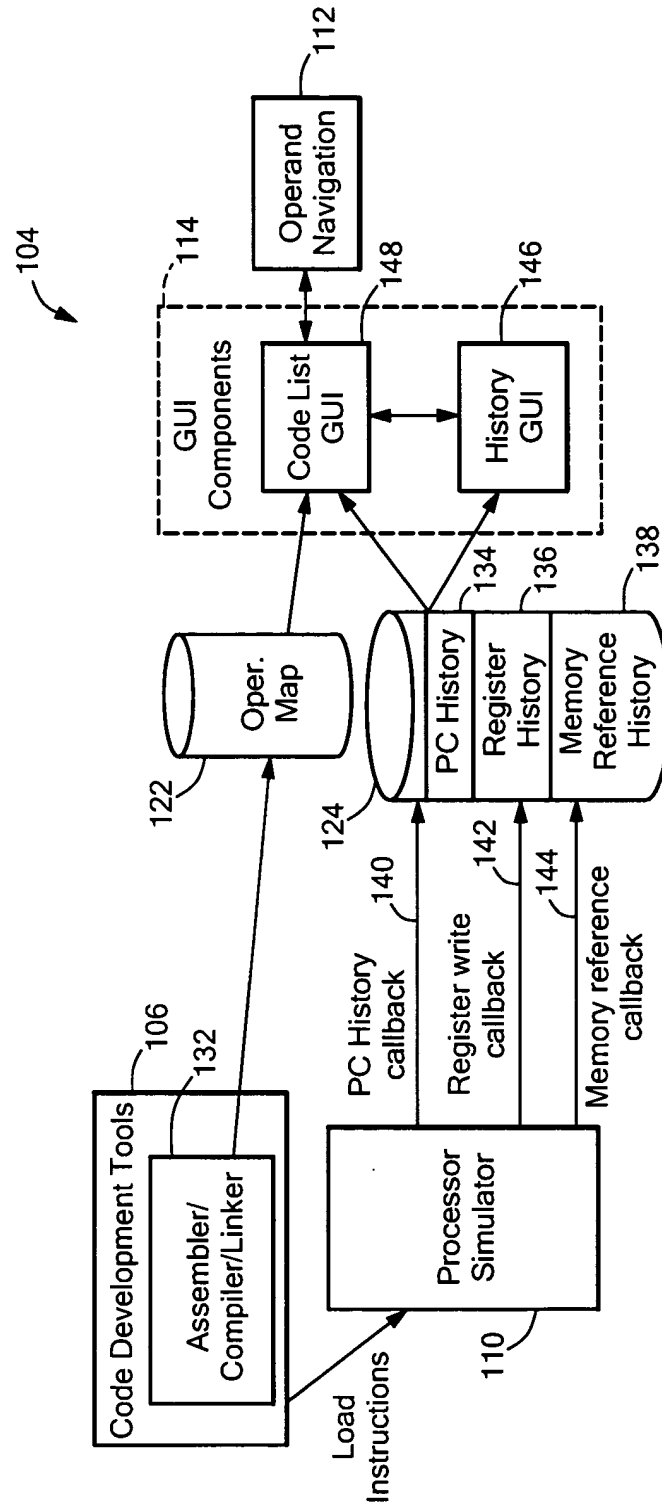


**FIG. 3**

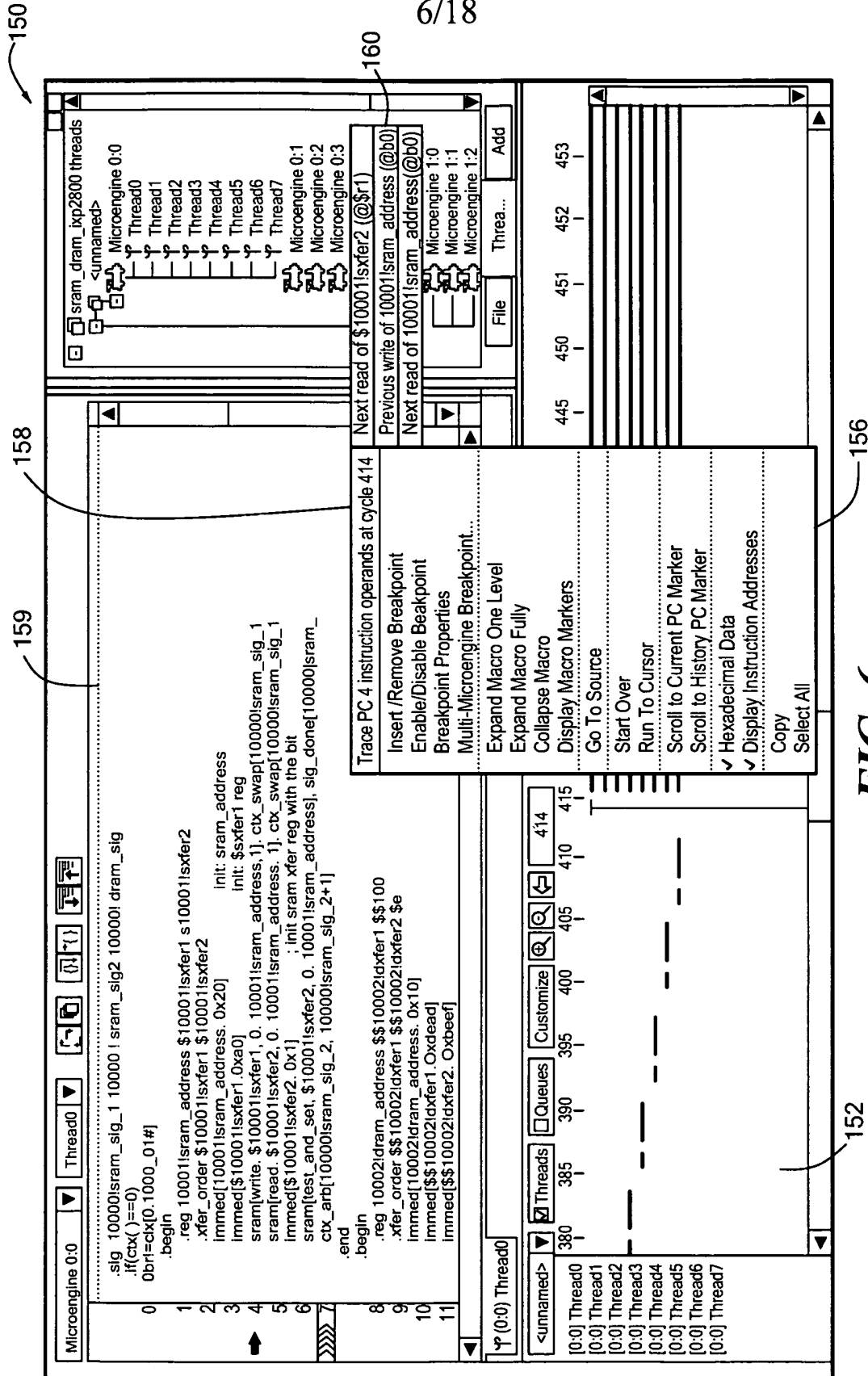


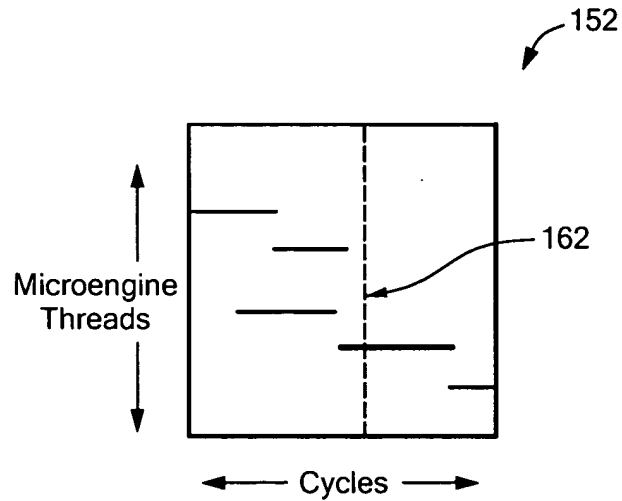
**FIG. 4**

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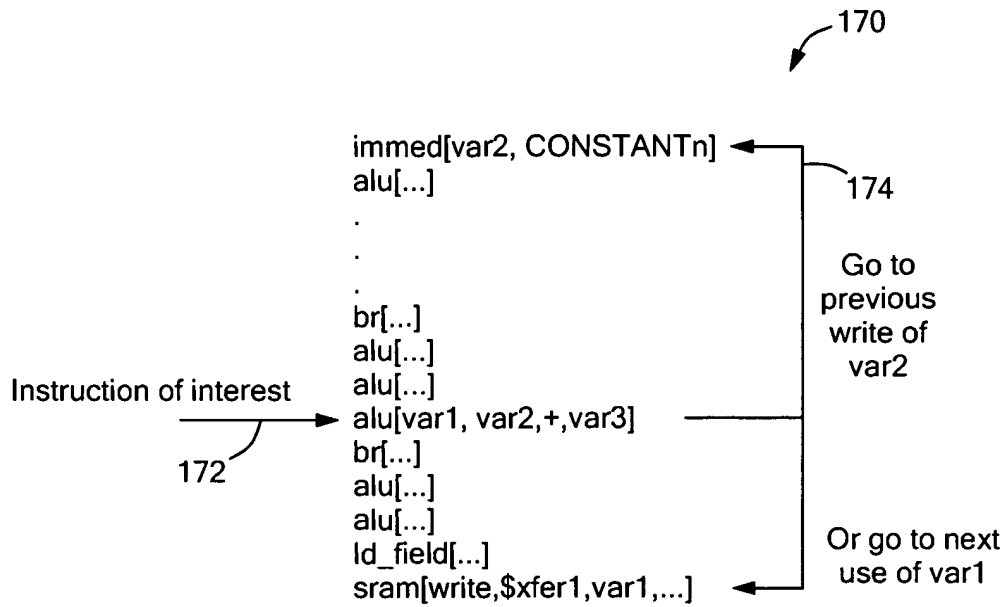


**FIG. 5**





**FIG. 7**



**FIG. 8**

122

PC	Src1	Src2	Dest	I/O Xfer	Xfer Count	I/O Direction
...						

180

182 184 186 188 190 192 194

FIG. 9

134

Time/Cycle	Thread State	Thread Number	PC
0	Executing	0	0
1	Executing	0	1
2	Aborted	0	2
3	Executing	1	0
4	Executing	1	1
5	Aborted	1	2
...			

200

202 206 204 208

FIG. 10



136

Time/Cycle	New Value
0	1
11	2
200	6
202	-1
...	

222 224

220

**FIG. 11**

138

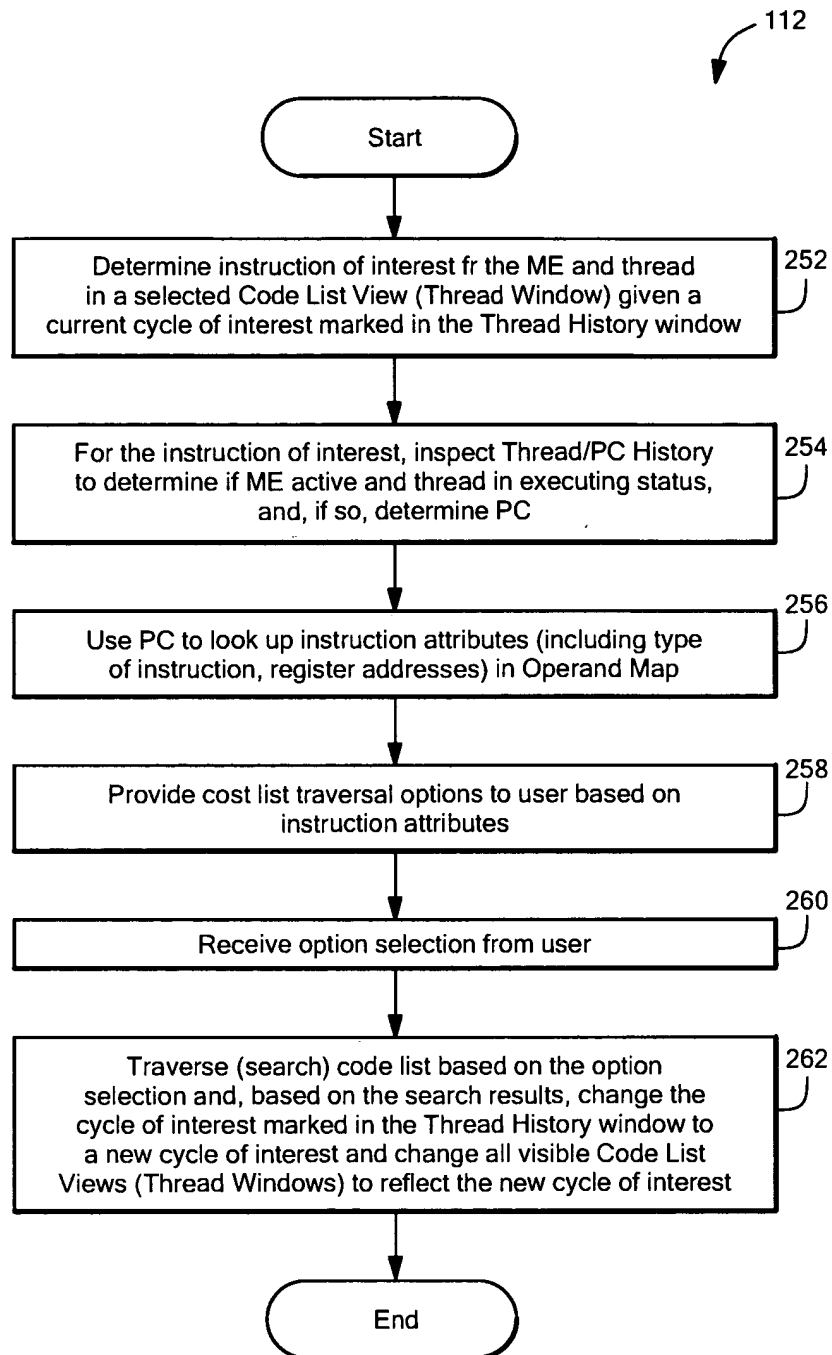
Creation Time/Cycle	PC	Xfer reg count	Primary Xfer reg no.	Primary Xfer reg ME	Remote Xfer reg no.	Remote Xfer reg ME
...						

232 234 236 238 240 242 244

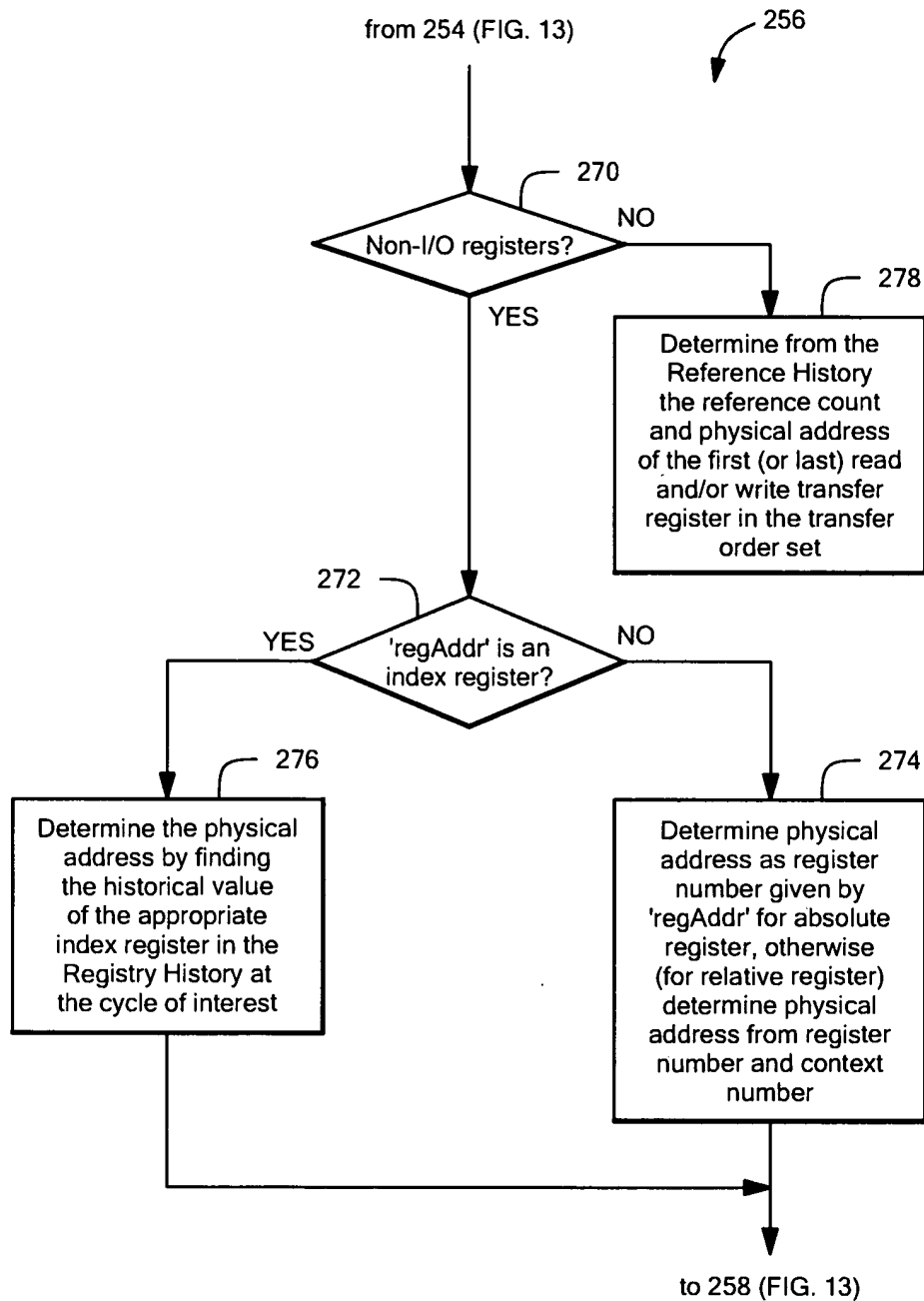
230

**FIG. 12**

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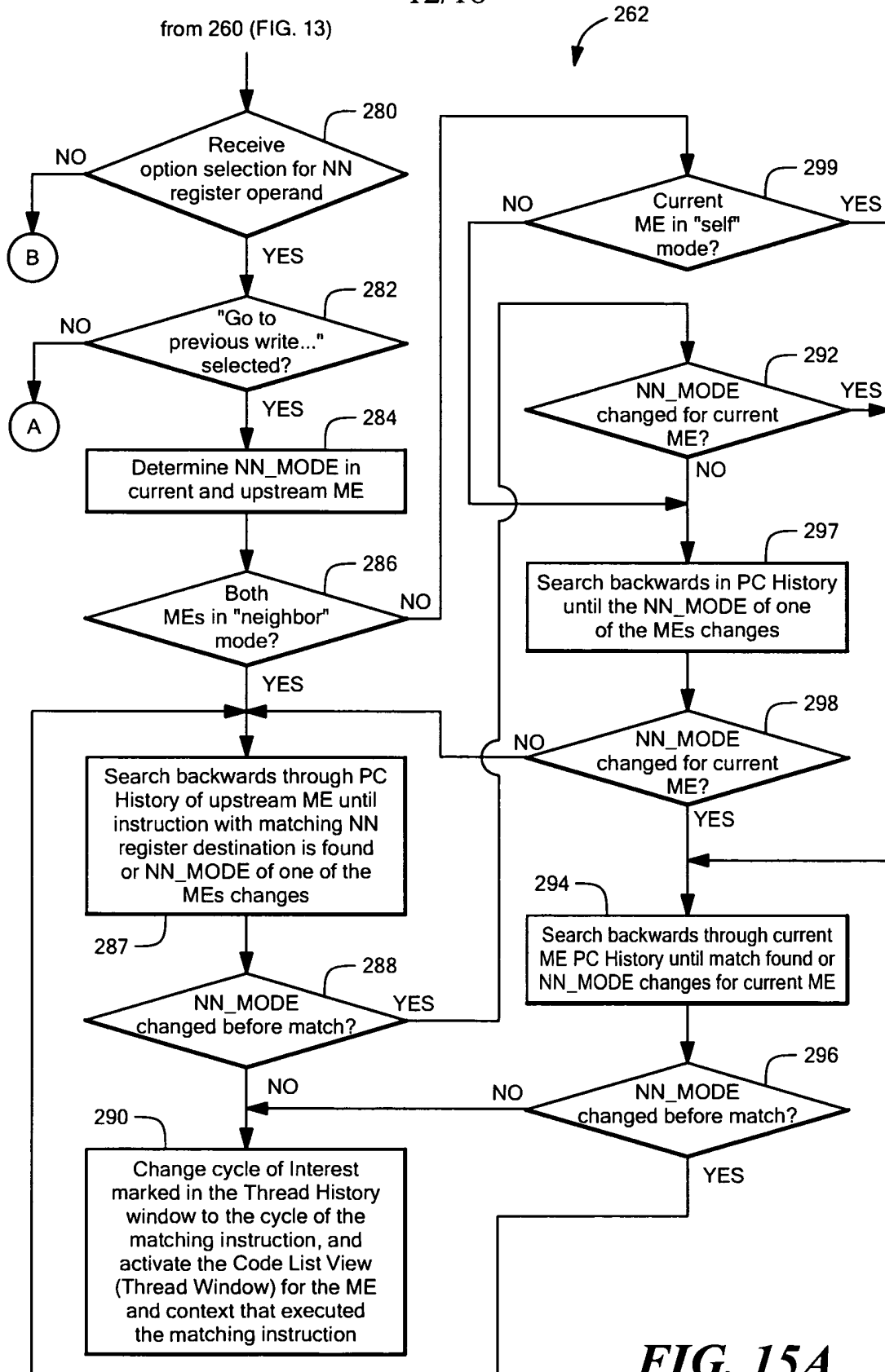


**FIG. 13**



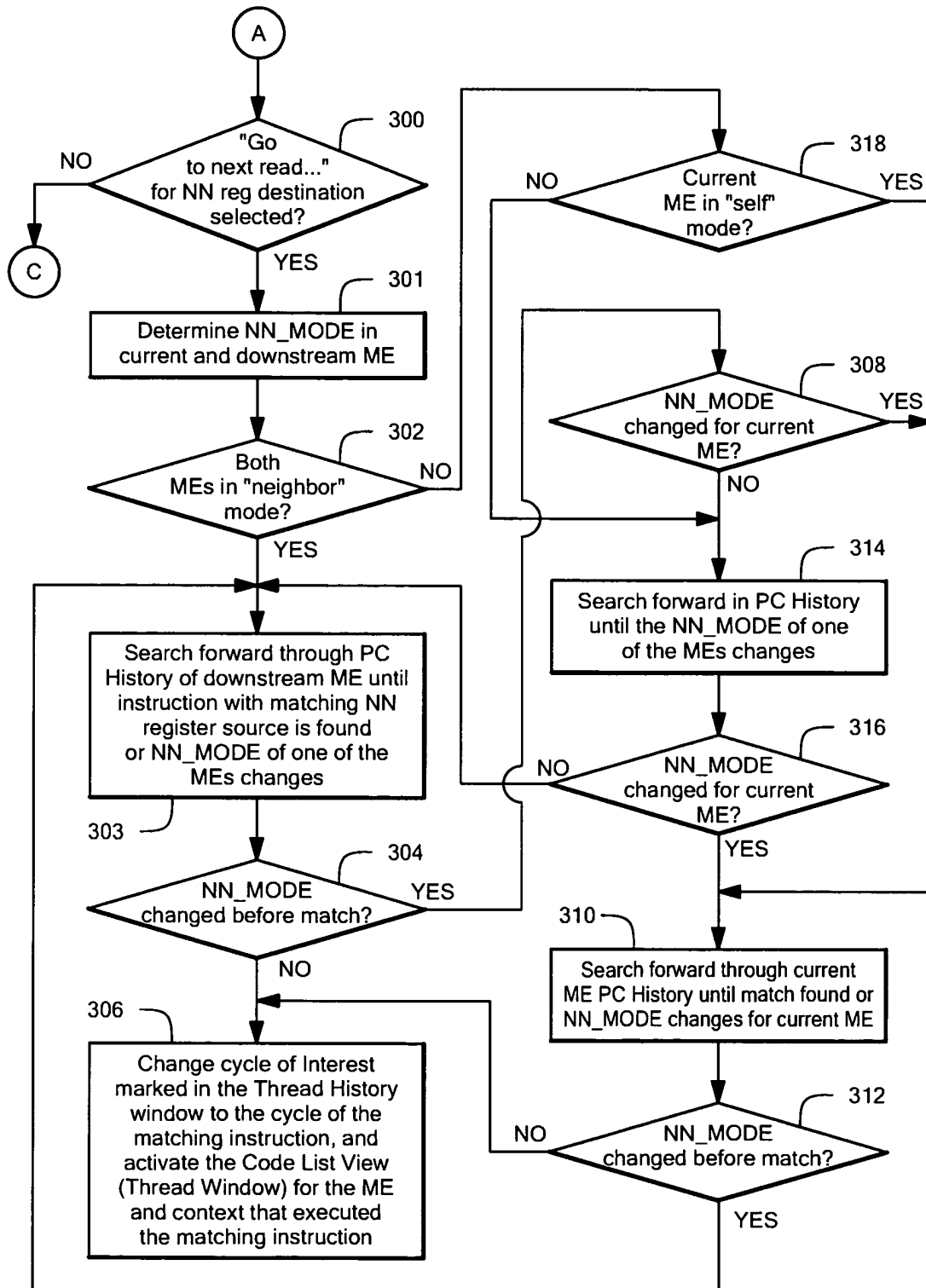
**FIG. 14**

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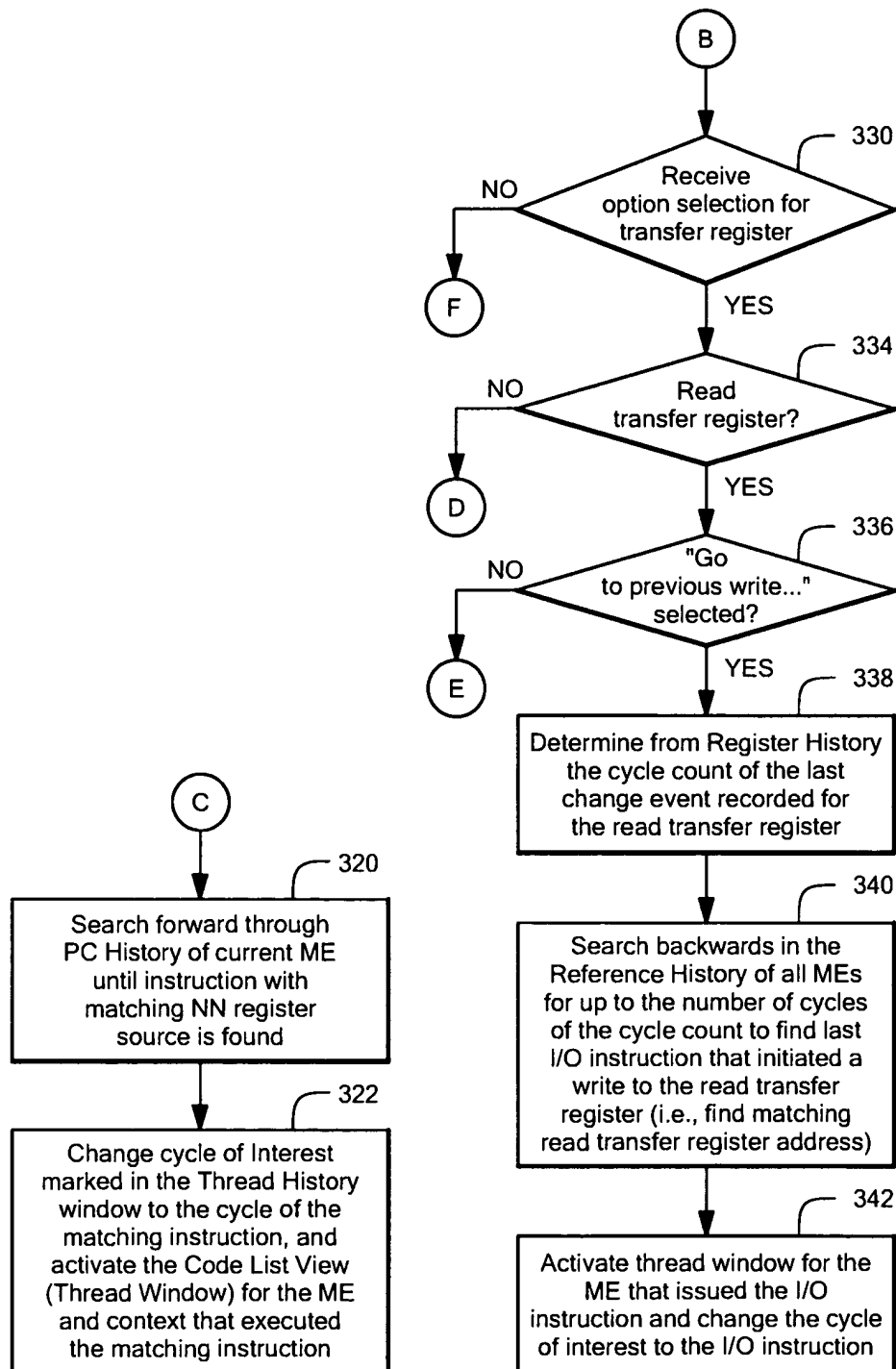


**FIG. 15A**

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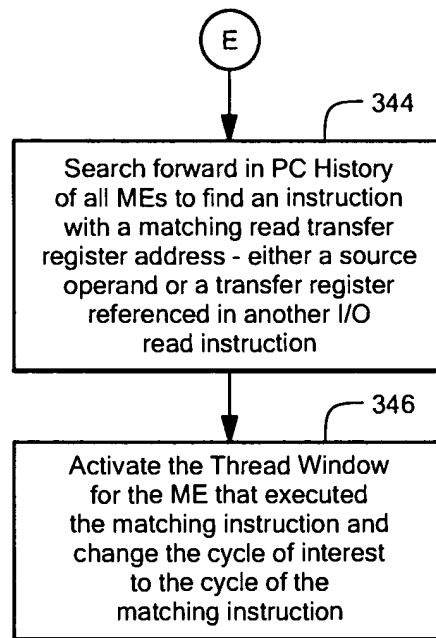


**FIG. 15B**

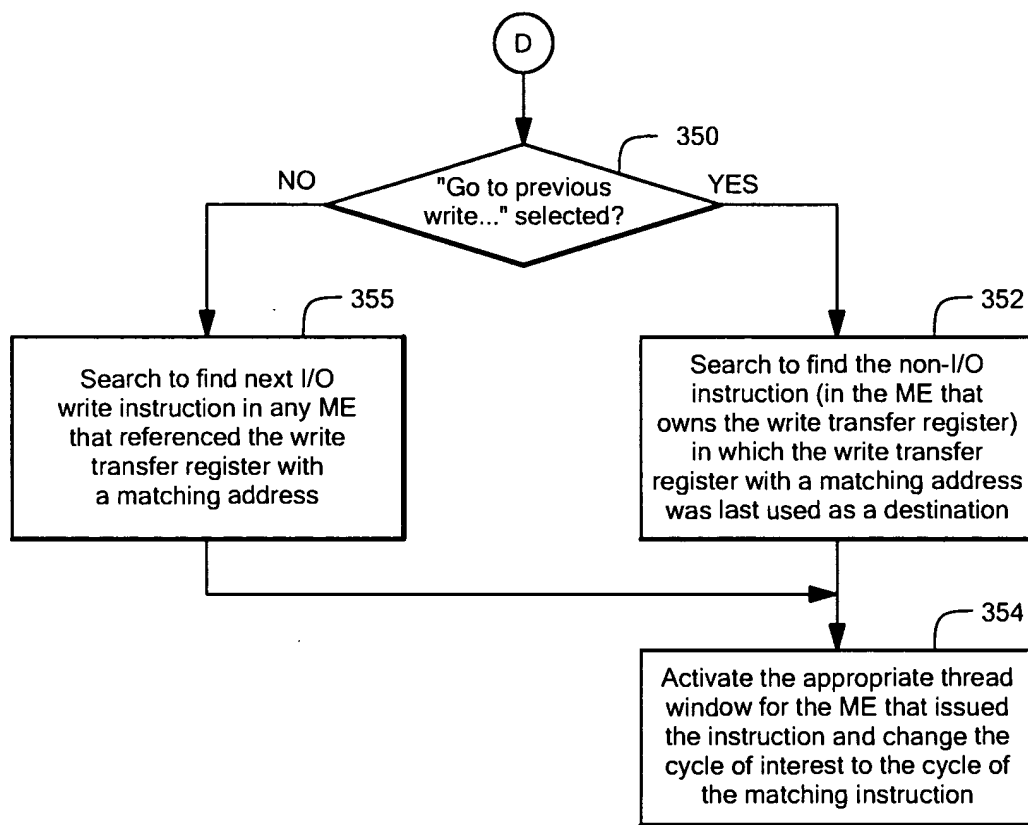


**FIG. 15C**

**FIG. 15D**

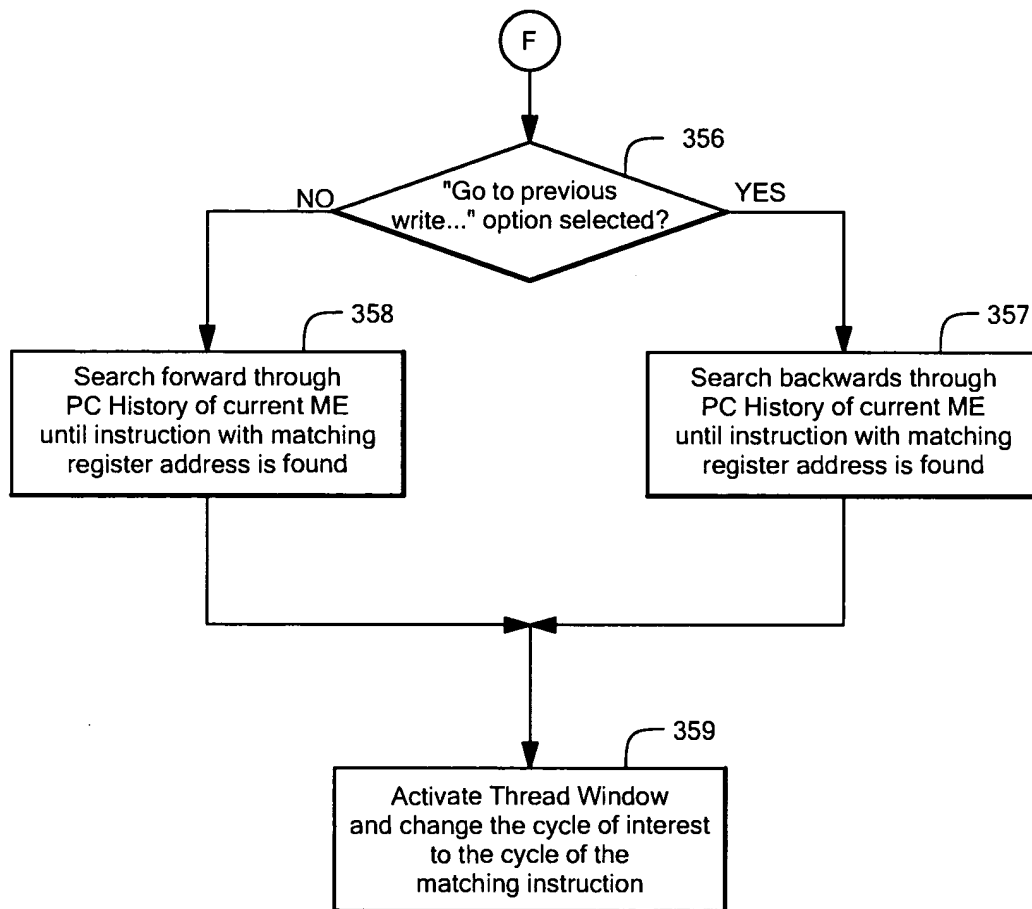


***FIG. 15E***

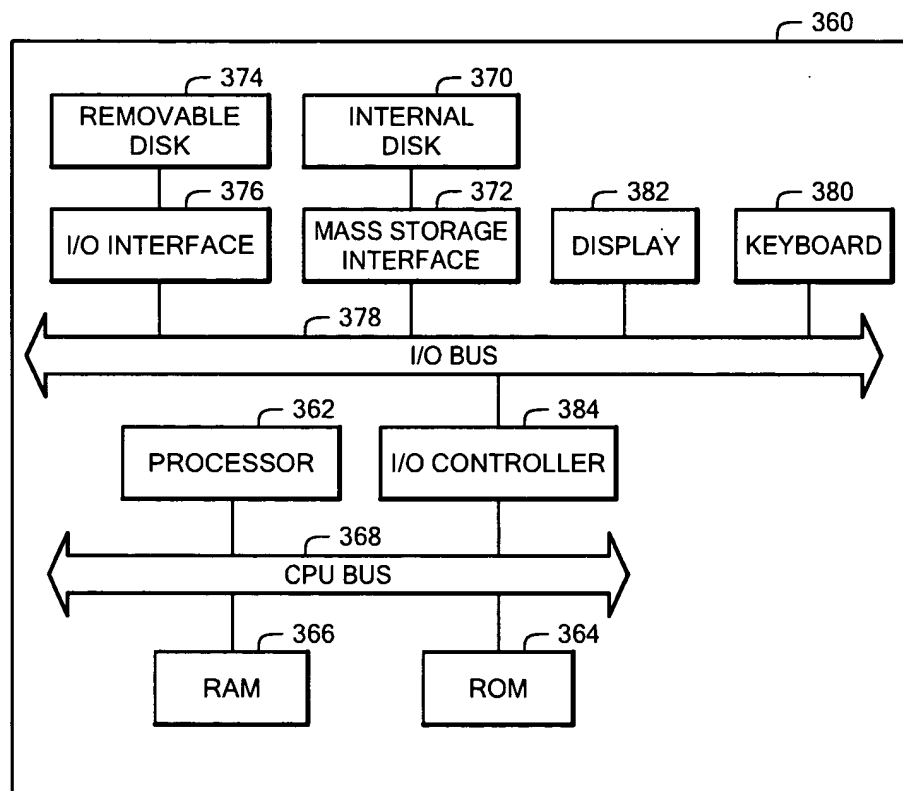


**FIG. 15F**





**FIG. 15G**



**FIG. 16**